

# Technical Reference Note

## AET (30W) Family



### ***AET (30W) Isolated DC/DC Converter Module Industry Standard Size, 2" x 1.6" x 0.48" 9-36V/18-75V Inputs, 2.5V/3.3V/5V/12V/±12V/15V/±15V Outputs***

The AET (30W) Isolated DC/DC Converter is 4:1 wide input voltage family for low power applications. With efficiency up to 84% typical for 5V module, this product is allowed to work at operating temperature range from -40°C to 71°C and a wide input voltage range of 4:1. Single-output and dual-output models are available for a wide range of applications in telecommunication, transportation equipment, etc.. Housed in small package, 2" x 1.6" x 0.48", with industry standard pinout, AET family eases the PCB designs and mechanical designs of customers' end products.



**Industry Standard Size  
2" x 1.6" x 0.48"**

### Electrical Parameters

#### Input

Input range	9-36 VDC; 18-75 VDC
Input Surge	50V / 100ms; 100V / 100ms
Efficiency	84% @5V (Typical)

#### Output

Regulation (Line, Load, Temp)	<1.5%
Ripple and noise	100mV p-p max
Output Voltage Adjust Range	± 10% of nominal output
Transient Response	5% max deviation with 50% load to full load 300uS (max) recovery
Short Circuit Protection	Indefinite
Over Voltage Protection	120-140% nominal output
Over Temperature Protection	100 to 120°C case surface

### Special Features

- Wide 4:1 input range
- High efficiency, 84%@5V
- 40°C to 90°C case surface operating temperature
- Input / Output isolation 1.5KVdc
- Low output ripple and noise
- Shielded metal case with size (2"x1.6"x0.48")
- Industrial standard pinout
- Lead-free soldering pins
- Fixed switching frequency (300KHz)
- Positive enable function
- Adjustable Output Voltage
- Built-in input filter meets EN55022 / FCC Class A without external components

### Environmental Specifications

- Operating temperature: -40°C to +71°C
- Storage temperature: -55°C to +105°C
- RoHS compliant

### Safety

Designed to meet EN60950



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### AET (30W) SERIES

THIS SPECIFICATION COVERS THE REQUIREMENTS  
FOR AN INDUSTRY STANDARD PACKAGE OF 2"x1.6"x0.48", 4:1 INPUT RANGE,  
30W, SINGLE OUTPUT AND DUAL OUTPUT ISOLATED DC/DC CONVERTER

#### PART NUMBERS

MODEL NAME / SIS CODE	Nominal Vin ( $V_{I, \text{nom}}$ ) / Range of Vin	$V_{\text{out}} / I_{\text{out}}$
AET08G18-L	24V / 9-36V	2.5V / 8A
AET07F18-L	24V / 9-36V	3.3V / 7A
AET06A18-L	24V / 9-36V	5V / 6A
AET02B18-L	24V / 9-36V	12V / 2.5A
AET01BB18-L	24V / 9-36V	$\pm 12V / \pm 1.25A$
AET02C18-L	24V / 9-36V	15V / 2A
AET01CC18-L	24V / 9-36V	$\pm 15V / \pm 1A$
AET08G36-L	48V / 18-75V	2.5V / 8A
AET07F36-L	48V / 18-75V	3.3V / 7A
AET06A36-L	48V / 18-75V	5V / 6A
AET02B36-L	48V / 18-75V	12V / 2.5A
AET01BB36-L	48V / 18-75V	$\pm 12V / \pm 1.25A$
AET02C36-L	48V / 18-75V	15V / 2A
AET01CC36-L	48V / 18-75V	$\pm 15V / \pm 1A$

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### ELECTRICAL SPECIFICATIONS

**STANDARD TEST CONDITION** on a single unit, unless otherwise specified.

$T_A$ :	25°C (Ambient Air)
+Vin :	24V $\pm 2\%$ (AETxxxx18-L)
	48V $\pm 2\%$ (AETxxxx36-L)
-Vin :	Return pin for +Vin
Enable:	Open (Positive Enable)
+Vout :	Connect to load
-Vout :	Connect to load (return)
Trim (Vadj) :	Open

### ABSOLUTE MAXIMUM RATINGS

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the IPS. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Device	Symbol	Min	Typ	Max	Unit
a) Input Voltage: Continuous: Transient (100ms)	AETxxxx18-L AETxxxx18-L	$V_I$ $V_{I, \text{trans}}$	0 0	- -	36 50	$V_{dc}$ $V_{dc}$
Continuous: Transient (100ms)	AETxxxx36-L AETxxxx36-L	$V_I$ $V_{I, \text{trans}}$	0 0	- -	75 100	$V_{dc}$ $V_{dc}$
b) Operating Temperature Ambient Case Surface	All	$T_A$ $T_C$	-40 -40	- -	71 100	°C °C
c) Storage Temperature	All	$T_{STG}$	-55	-	105	°C
d) Operating Humidity	All	-	-	-	95	%
e) I/O Isolation (Conditions : 0.5mA for 60 sec) Input-Output	All	-	-	-	1500	$V_{dc}$
f) Output Power	2.5V 3.3V Others	$P_{o, \text{max}}$ $P_{o, \text{max}}$ $P_{o, \text{max}}$	- - -	- - -	20 23.1 30	W W W

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### INPUT SPECIFICATIONS

Parameter	Device	Symbol	Min	Typ	Max	Unit
a) Operating Input Voltage	AETxxxx18-L AETxxxx36-L	V <sub>I</sub> V <sub>I</sub>	9 18	24 48	36 75	V <sub>dc</sub> V <sub>dc</sub>
b) Maximum Input Current						
	AETxxxx18-L (V <sub>I</sub> = 0 to V <sub>I, max</sub> ; I <sub>o</sub> = I <sub>o, max</sub> )	2.5V 3.3V 5V 12V / ±12V 15V / ±15V	I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub>	- - - - -	3.5 4 5 5 5	A A A A A
	AETxxxx36-L (V <sub>I</sub> = 0 to V <sub>I, max</sub> ; I <sub>o</sub> = I <sub>o, max</sub> )	2.5V 3.3V 5V 12V / ±12V 15V / ±15V	I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub> I <sub>I, max</sub>	- - - - -	2 2.5 3 3 3	A A A A A
c) No Load Input Power	All	-	-	-	0.5	W
	(V <sub>I</sub> = V <sub>I, nom</sub> )					
d) Recommended External Fuse Ratings						
	AETxxxx18-L	2.5V 3.3V 5V 12V / ±12V 15V / ±15V		4 5 6 6 6	-	A
	AETxxxx36-L	2.5V 3.3V 5V 12V / ±12V 15V / ±15V		3 3 4 4 4	-	A

**CAUTION: This power module is not internally fused. An input fuse must always be used.**

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### OUTPUT SPECIFICATIONS

Parameter	Device	Symbol	Min	Typ	Max	Unit
a) Output Voltage Setpoint ( $V_I = V_{I,\min}$ to $V_{I,\max}$ ; $I_O = I_{O,\max}$ ; $T_A = 25^\circ\text{C}$ )	2.5V 3.3V 5V 12V 15V $\pm 12\text{V}$ $\pm 15\text{V}$	$V_{o,\text{set}}$ $V_{o,\text{set}}$ $V_{o,\text{set}}$ $V_{o,\text{set}}$ $V_{o,\text{set}}$ $V_{o,\text{set}}$ $V_{o,\text{set}}$	2.46 3.25 4.92 11.82 14.77 $\pm 11.82$ $\pm 14.77$	2.50 3.30 5.00 12.00 15.00 $\pm 12.00$ $\pm 15.00$	2.54 3.35 5.08 12.18 15.23 $\pm 12.18$ $\pm 15.23$	$V_{dc}$ $V_{dc}$ $V_{dc}$ $V_{dc}$ $V_{dc}$ $V_{dc}$ $V_{dc}$
b) Output Regulation: Line ( $V_I = V_{I,\max}$ to $V_{I,\min}$ ; $I_O = I_{O,\max}$ )	All	-	-	-	0.5	%
Load ( $V_I = V_{I,\text{nom}}$ ; $I_O = I_{O,\min}$ to $I_{O,\max}$ )	All	-	-	-	0.5	%
Cross ( $V_I = V_{I,\text{nom}}$ ; $I_O = +I_{O,\max}, -I_{O,\min}$ or $+I_{O,\min}, -I_{O,\max}$ to $+I_{O,\max}, -I_{O,\min}$ )	$\pm 12\text{V}/\pm 15\text{V}$	-	-	-	4	%
Temperature ( $T_c = -40^\circ\text{C}$ to $+100^\circ\text{C}$ )	All	-	-	-	1	% $V_o$
c) Output Ripple and Noise (Across 1 $\mu\text{F}$ @50V, X7R ceramic capacitor & 10 $\mu\text{F}$ @25V tantalum capacitor) See Figure 1. Peak-to-Peak (5 Hz to 20 MHz)	All	-	-	-	100	mVp-p
d) Rated Output Current Single Output	2.5V 3.3V 5V 12V 15V	$I_o$	800 700 600 250 200	-	8000 7000 6000 2500 2000	mA
Dual Output	$\pm 12\text{V}$ $\pm 15\text{V}$	$I_o$	$\pm 125$ $\pm 100$	-	$\pm 1250$ $\pm 1000$	mA
e) Efficiency ( $V_I = V_{I,\text{nom}}$ ; $I_{O,\max}$ ; $T_A = 25^\circ\text{C}$ )	2.5V 3.3V 5V 12V 15V $\pm 12\text{V}$ $\pm 15\text{V}$	-	-	79 82 84 85 85 85 85	-	%
f) Switching Frequency	All	-	270	300	330	KHz

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### OUTPUT SPECIFICATIONS (Cont.)

Parameter	Device	Symbol	Min	Typ	Max	Unit
g) Dynamic Response : ( $\Delta I_o / \Delta t = 0.08 A/\mu s$ ; $V_I = V_{I, nom}$ ; $T_A = 25^\circ C$ )						
Load Change from $I_o = 50\%$ to 100% of $I_{o, max}$	2.5V/3.3V/5V 12V/ $\pm 12V$ /15V/ $\pm 15V$	- -	- -	- -	5 2	%Vo
Peak Deviation Settling Time (to $V_{o, nom}$ )	All	-	-	-	300	$\mu sec$
h) Turn-On Time ( $I_o = I_{o, max}$ ; Vo within 1%)	All	-	-	6	10	msec
i) Output Voltage Overshoot ( $I_o = I_{o, max}$ ; $T_A = 25^\circ C$ )	All	-	-	1	4	%Vo

### FEATURE SPECIFICATIONS

Parameter	Device	Symbol	Min	Typ	Max	Unit
Enable Pin Voltage						
Logic Low	All	OFF	-0.7	-	1.0	V
Logic High	All	ON	3.0	-	$V_I$	V
OPEN	All	ON	-	-	-	
Enable Pin Current						
Logic Low	All	OFF	-	-	2	mA
Logic High (leakage current, @ $V_I$ )	All	ON	-	-	50	uA
OPEN	All	ON	-	-	0	uA
Output Voltage Adjustment Range	All	-	90	-	110	%Vo
Output Overvoltage Clamp	2.5V 3.3V 5V 12V 15V $\pm 12V$ $\pm 15V$	$V_o$ , clamp	3.0 4.0 6.0 14.4 18.0 $\pm 14.4$ $\pm 18.0$	- - - - - - -	3.5 4.6 7.0 16.8 20.0 $\pm 16.8$ $\pm 20.0$	V
Overtemperature Clamp	All	$T_c$ , clamp	100	-	120	°C

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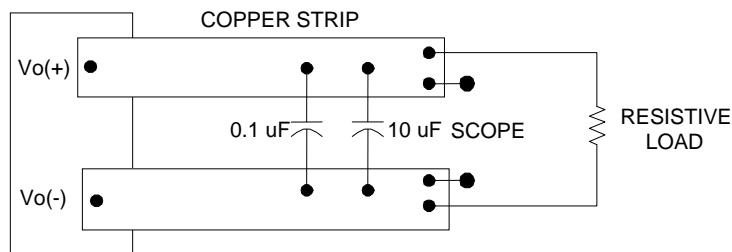
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### FEATURE SPECIFICATIONS (Cont.)

Undervoltage Lockout							
Turn-on Point	AETxxxx18-L	-	-	8.7	9	V	
	AETxxxx36-L	-	-	16.5	18	V	
Turn-off Point	AETxxxx18-L	-	6.5	8.5	-	V	
	AETxxxx36-L	-	13	15	-	V	
Isolation Capacitance	All	-	-	1000	-	PF	
Isolation Resistance	All	-	10	-	-	MΩ	
Calculated MTBF (Io = I <sub>o, max</sub> ; T <sub>A</sub> = 25°C)	All	-	400K	500K	-	Hours	
Weight	All	-	-	-	70	g	

### TEST SETUP



Note: Use a 0.1μF @50V X7R ceramic capacitor and a 10μF @25V tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load between 51 mm and 76 mm (2 in. and 3 in.) from module.

Figure 1 : Peak-to-Peak Output Noise Measurement Test Setup

## **Basic Operation and Features**

The AET converters were designed specifically to address applications where high power density is required. These modules provide 1500Vdc isolation and operate from the input ranges of 9V-36V and 18V-75V with standard features such as Enable, Trim, OCP, OVP and OTP.

## **Output Overcurrent Protection**

To provide protection in an output overload or short circuit condition, the converter is equipped with current limiting circuitry and can endure the fault condition for an unlimited duration. At the point of current-limit inception, the converter goes into “Hiccup Mode”, causing the output current to be limited both in peak and duration. The converter will operate back normally once the output current is brought back into its specified range.

## **Output Overvoltage Protection**

The output overvoltage system consists of a separate control loop, independent of the primary control loop. This control loop has a higher voltage set point than the primary loop. In a fault condition of output overvoltage, the converter latches and clamps the output to a lower voltage level to ensure that the output voltage does not exceed  $V_o$ , clamp, max. The converter will operate back normally once the fault is removed and the input voltage is cycled or the enable pin is toggled.

## **Overtemperature Protection**

In a fault condition of overtemperature, the converter goes into “Hiccup Mode” to reduce heat generated internally and ensure that the case temperature does not exceed  $T_c$ , clamp, max. The converter will operate back normally once the case temperature is brought back below  $T_c$ , clamp, min.

## **Enable Function**

Positive Logic Enable turns the converter on during a logic-high voltage or an open circuit on the enable pin, and off during a logic-low.

## **Trim Function**

Output Voltage adjustment is accomplished by connecting an external resistor between the Trim Pin and either the  $+V_{out}$  or  $-V_{out}$  Pins.

**To adjust  $V_o$  to a higher value**, please refer to Figure 2. An external resistor,  $R_{adj\_up}$ , should be connected between the Trim Pin and the  $-V_{out}$  Pin. From equation (1),  $R_{adj\_up}$  resistor can be determined for the required output voltage.

### **Equation (1)**

$$\text{For } V_o=2.5\text{V modules, } V_o = \frac{3800}{15200+R_{adj\_up}} + 2.5$$

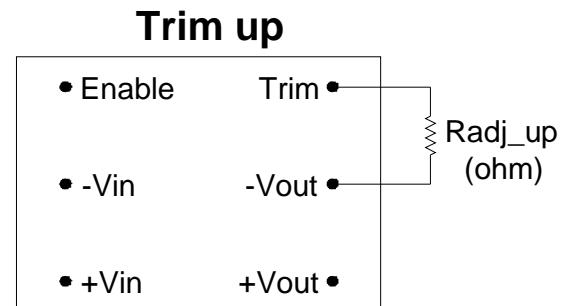


Figure 2. Circuit Configuration to Increase Output Voltage

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For  $V_o=3.3V$  modules,  $V_o = \frac{2393}{7250+R_{adj\_up}} + 3.3$

For  $V_o=5V$  modules,  $V_o = \frac{9550}{19100+R_{adj\_up}} + 5$

For  $V_o=12V$  modules,  $V_o = \frac{37200}{31000+R_{adj\_up}} + 12$

For  $V_o=15V$  modules,  $V_o = \frac{36450}{24300+R_{adj\_up}} + 15$

For  $V_o=\pm 12V$  modules,  $V_o = \pm \left| \left( \frac{87600}{36500+R_{adj\_up}} + 24 \right) \div 2 \right|$

For  $V_o=\pm 15V$  modules,  $V_o = \pm \left| \left( \frac{66300}{22100+R_{adj\_up}} + 30 \right) \div 2 \right|$

Where:  $R_{adj\_up}$  is in  $\Omega$

$V_o$  is not changed when  $R_{adj\_up}$  is not connected

110%  $V_o$  is obtained when  $R_{adj\_up} = 0 \Omega$  (i.e. Trim Pin is shorted to  $-V_{out}$  Pin)

**To adjust  $V_o$  to a lower value**, please refer to Figure 3.

An external resistor,  $R_{adj\_down}$ , should be connected between the Trim Pin and the  $+V_{out}$  Pin. From equation (2),  $R_{adj\_down}$  resistor can be determined for the required output voltage.

### Equation (2)

For  $V_o=2.5V$  modules,  $V_o = \frac{35100+2.5 R_{adj\_down}}{15600+R_{adj\_down}}$

For  $V_o=3.3V$  modules,  $V_o = \frac{43956+3.3 R_{adj\_down}}{14800+R_{adj\_down}}$

For  $V_o=5V$  modules,  $V_o = \frac{86400+5 R_{adj\_down}}{19200+R_{adj\_down}}$

For  $V_o=12V$  modules,  $V_o = \frac{1468800+12 R_{adj\_down}}{136000+R_{adj\_down}}$

For  $V_o=15V$  modules,  $V_o = \frac{1957500+15 R_{adj\_down}}{145000+R_{adj\_down}}$

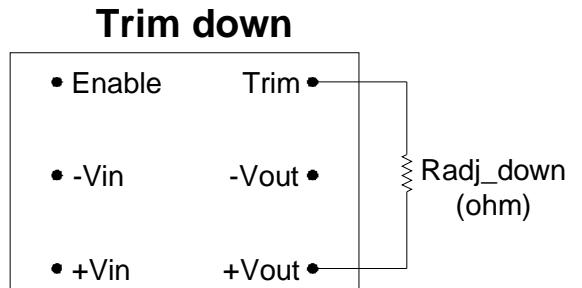


Figure 3. Circuit Configuration to Decrease Output Voltage

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For  $V_o = \pm 12V$  modules,  $V_o = \pm \left| \left( \frac{8152000 + 24 \text{ Radj\_down}}{377400 + \text{Radj\_down}} \right) \div 2 \right|$

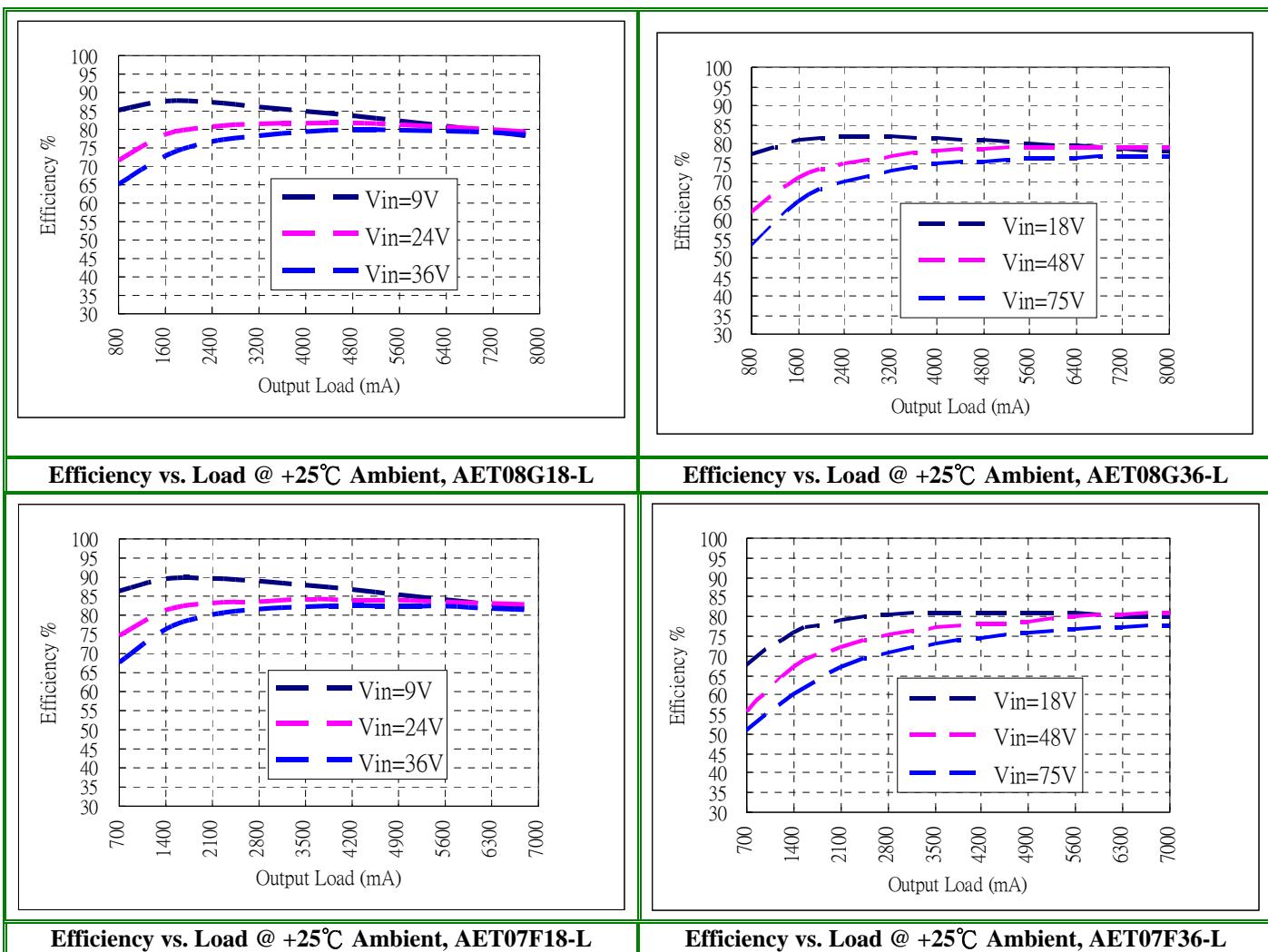
For  $V_o = \pm 15V$  modules,  $V_o = \pm \left| \left( \frac{7971000 + 30 \text{ Radj\_down}}{295220 + \text{Radj\_down}} \right) \div 2 \right|$

Where:  $\text{Radj\_down}$  is in  $\Omega$

$V_o$  is not changed when  $\text{Radj\_down}$  is not connected

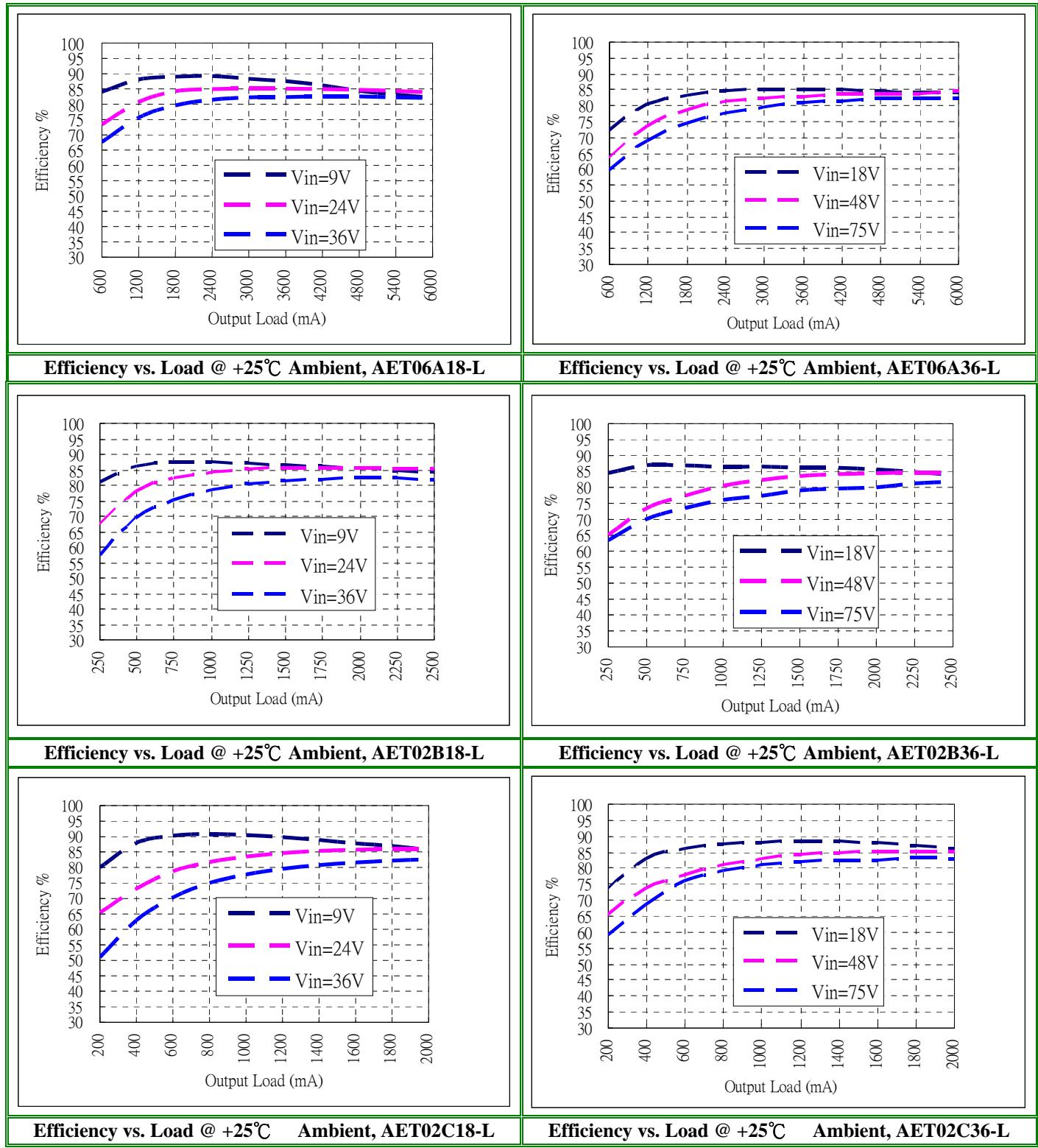
90%  $V_o$  is obtained when  $\text{Radj\_down} = 0 \Omega$  (i.e. Trim Pin is shorted to  $+V_{out}$  Pin)

### Performance Curves – Efficiency Curve



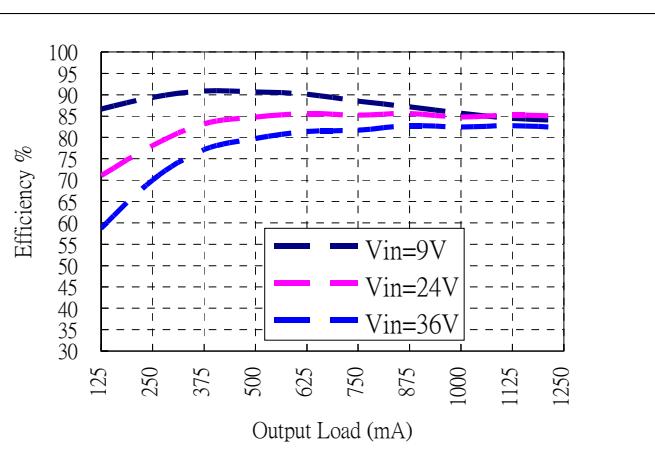
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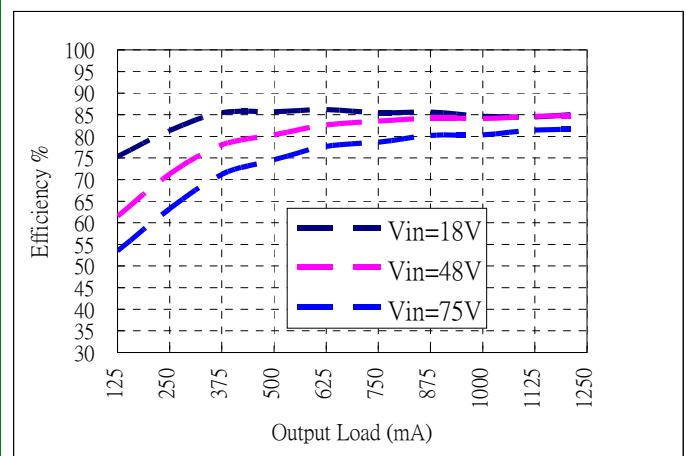


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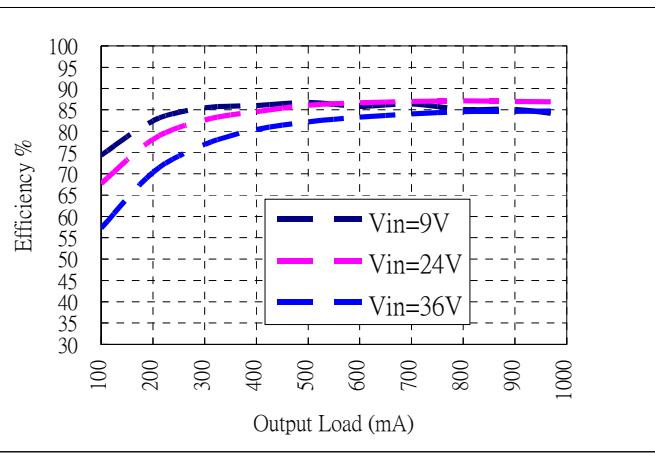
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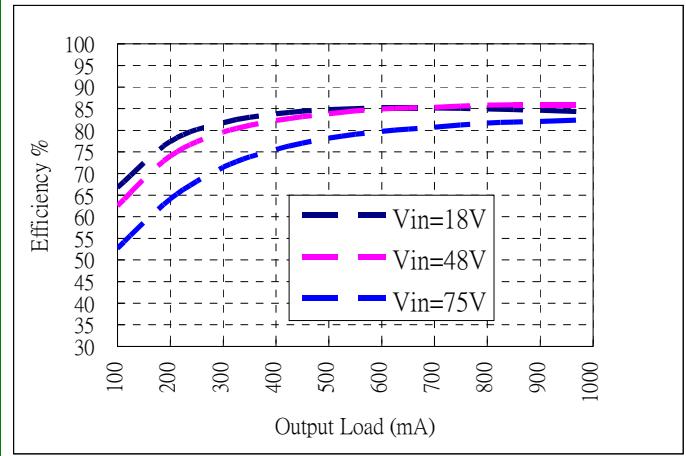
Efficiency vs. Load @ +25°C Ambient, AET01BB18-L



Efficiency vs. Load @ +25°C Ambient, AET01BB36-L



Efficiency vs. Load @ +25°C Ambient, AET01CC18-L



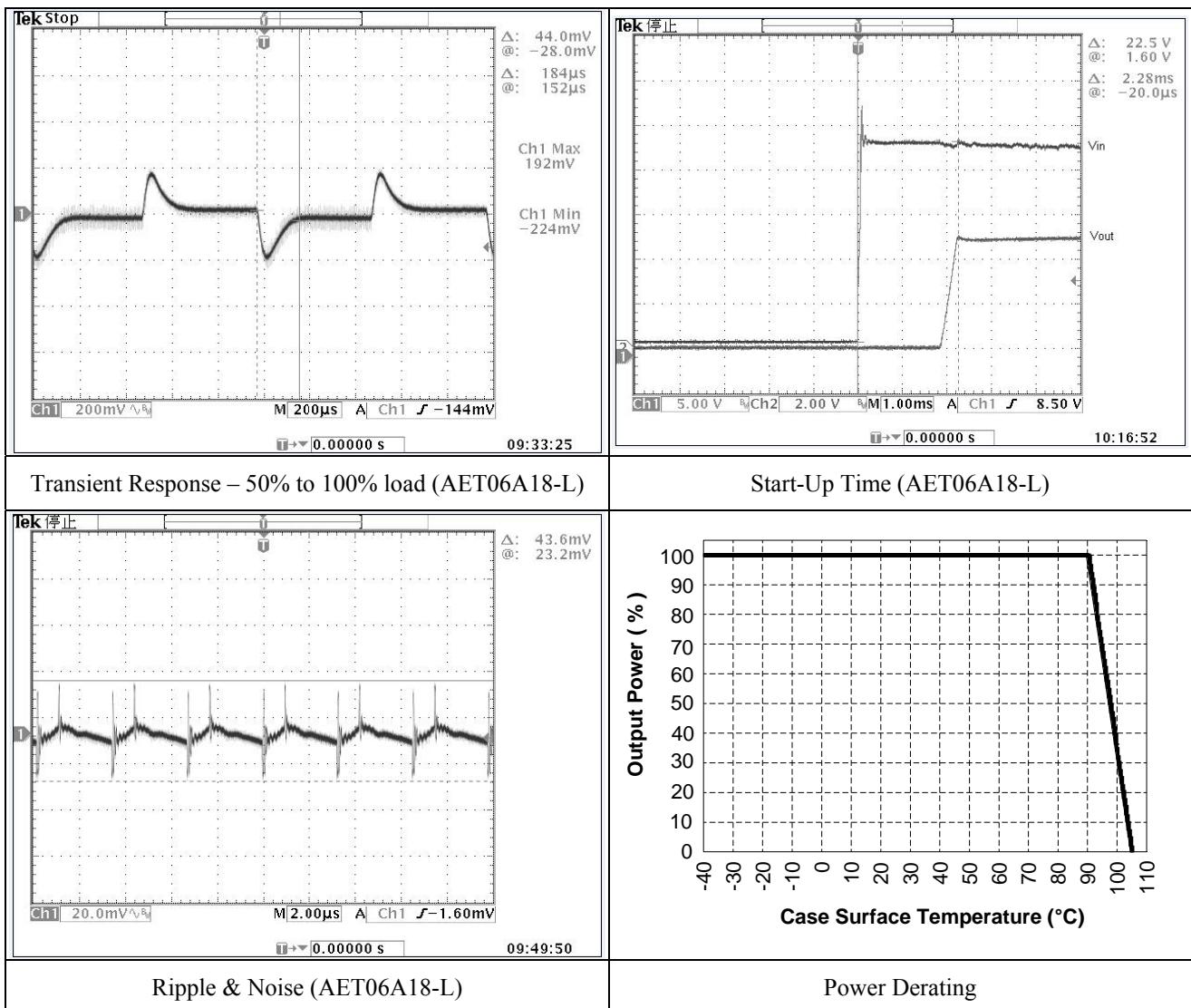
Efficiency vs. Load @ +25°C Ambient, AET01CC36-L

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### Performance Curves

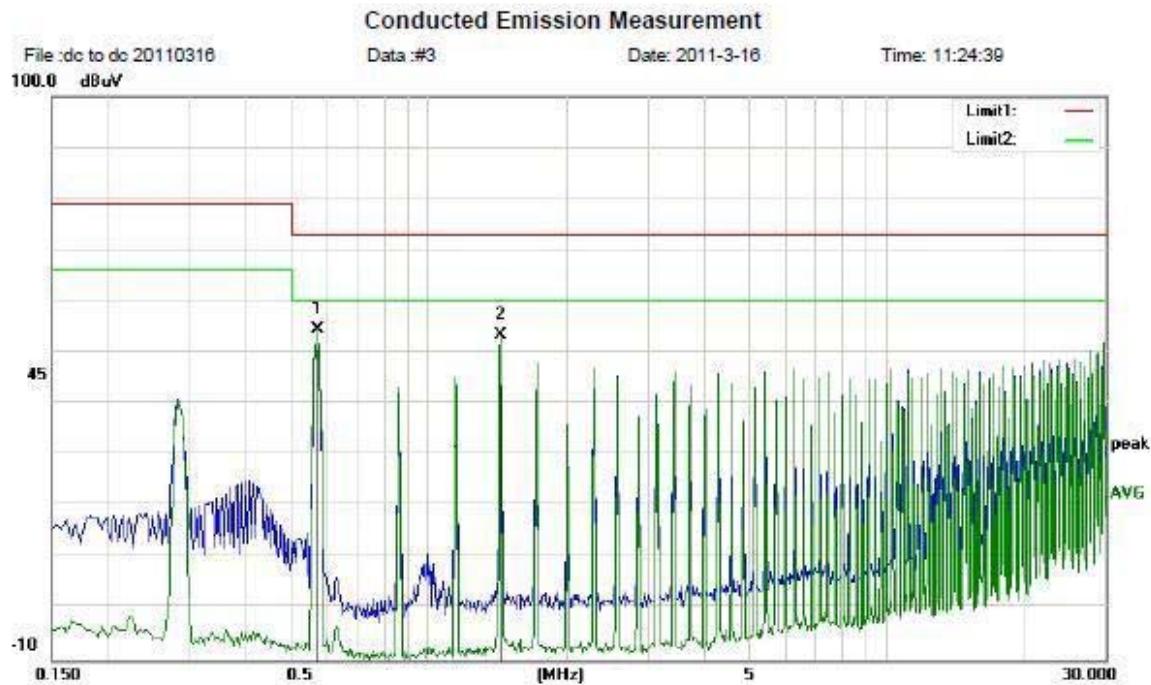


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### Conducted EMI Performance



No.	Mk.	Freq.	Reading Level	Correct Factor	Measure-ment	Limit	Over	
		MHz	dBuV	dB	dBuV	dB	Detector	Comment
1	*	0.5740	54.39	0.12	54.51	73.00	-18.49	peak
2		1.4340	53.27	0.20	53.47	73.00	-19.53	peak

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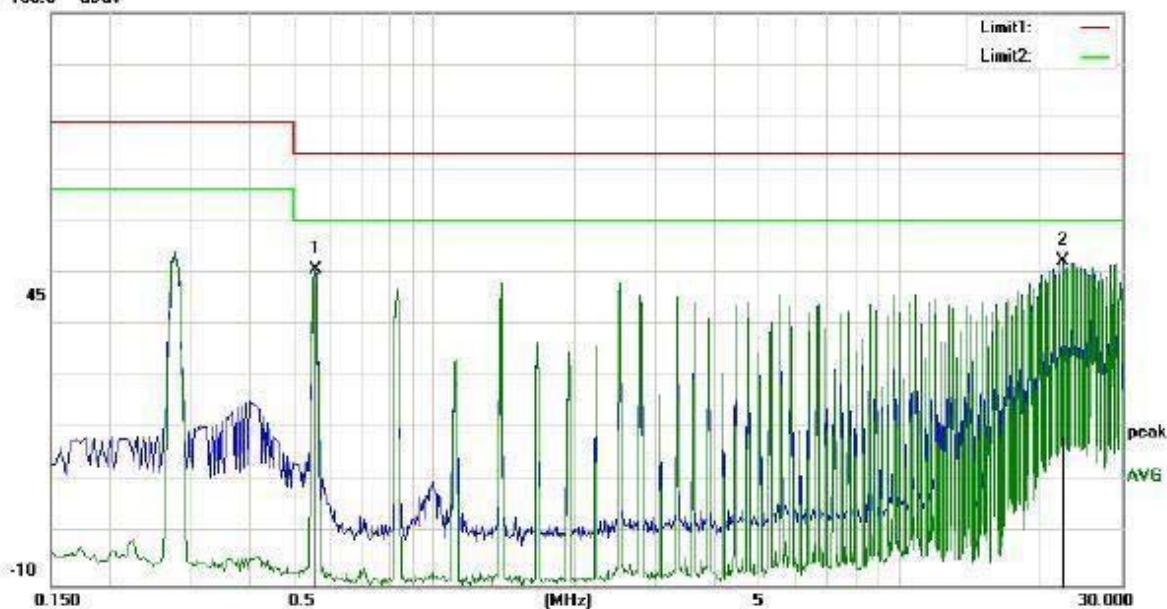
### Conducted Emission Measurement

File: dc to dc 20110316  
100.0 dBuV

Data #: 4

Date: 2011-3-16

Time: 11:33:25



Site Chamber #1

Phase: *N*

Temperature: 20

Limit: (CE)EN55022 class A\_QP

Power: DC 24V

Humidity: 57 %

EUT: dc to dc converter

Test Result: Pass

M/N: AET06A18-L

Mode: Full Load

Note:

No.	Mk.	Freq.	Reading Level	Correct Factor	Measure-ment	Limit	Over	Detector	Comment
		MHz	dBuV	dB	dBuV	dB			
1		0.5580	50.40	0.12	50.52	73.00	-22.48	peak	
2 *		22.4260	51.20	1.07	52.27	73.00	-20.73	peak	

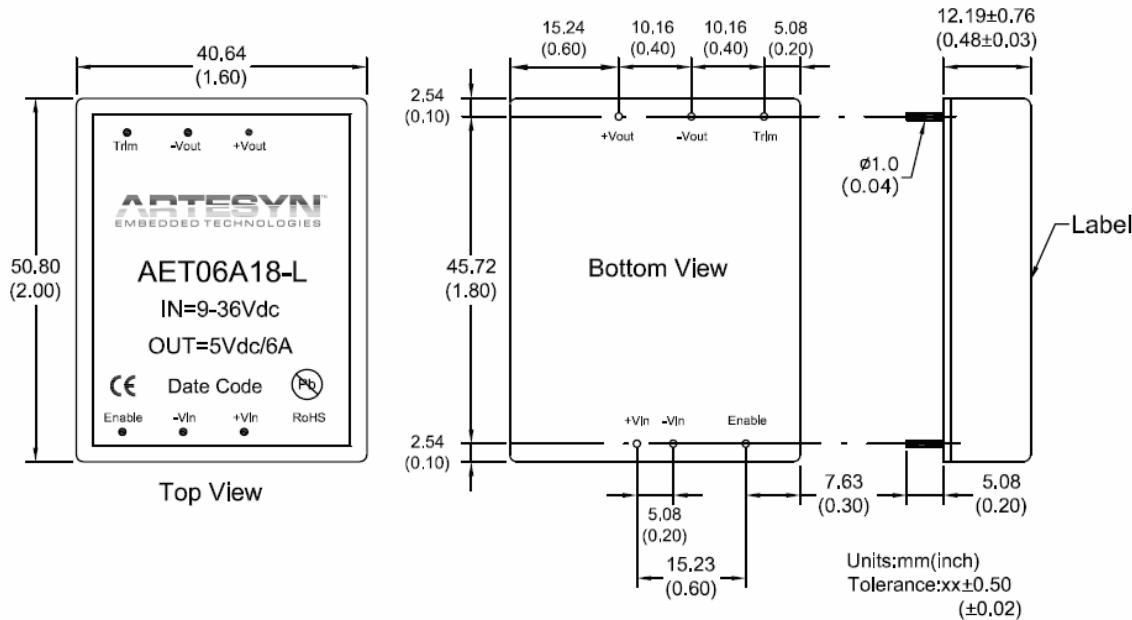
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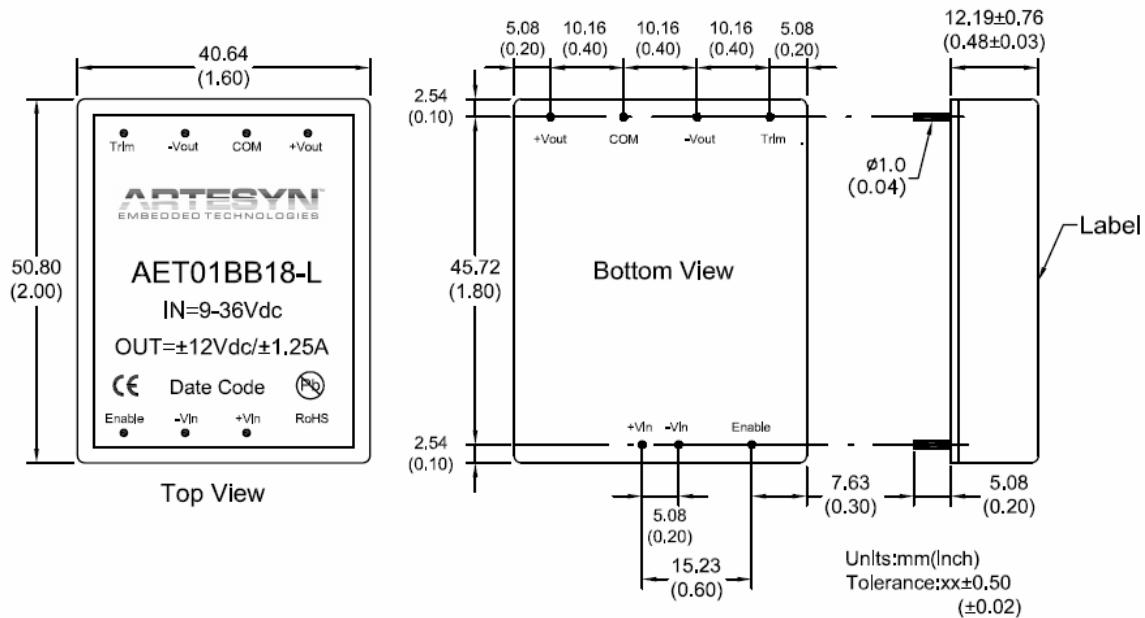
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### Mechanical Dimensions and Module Pin Assignment

#### Single Output



#### Dual Output

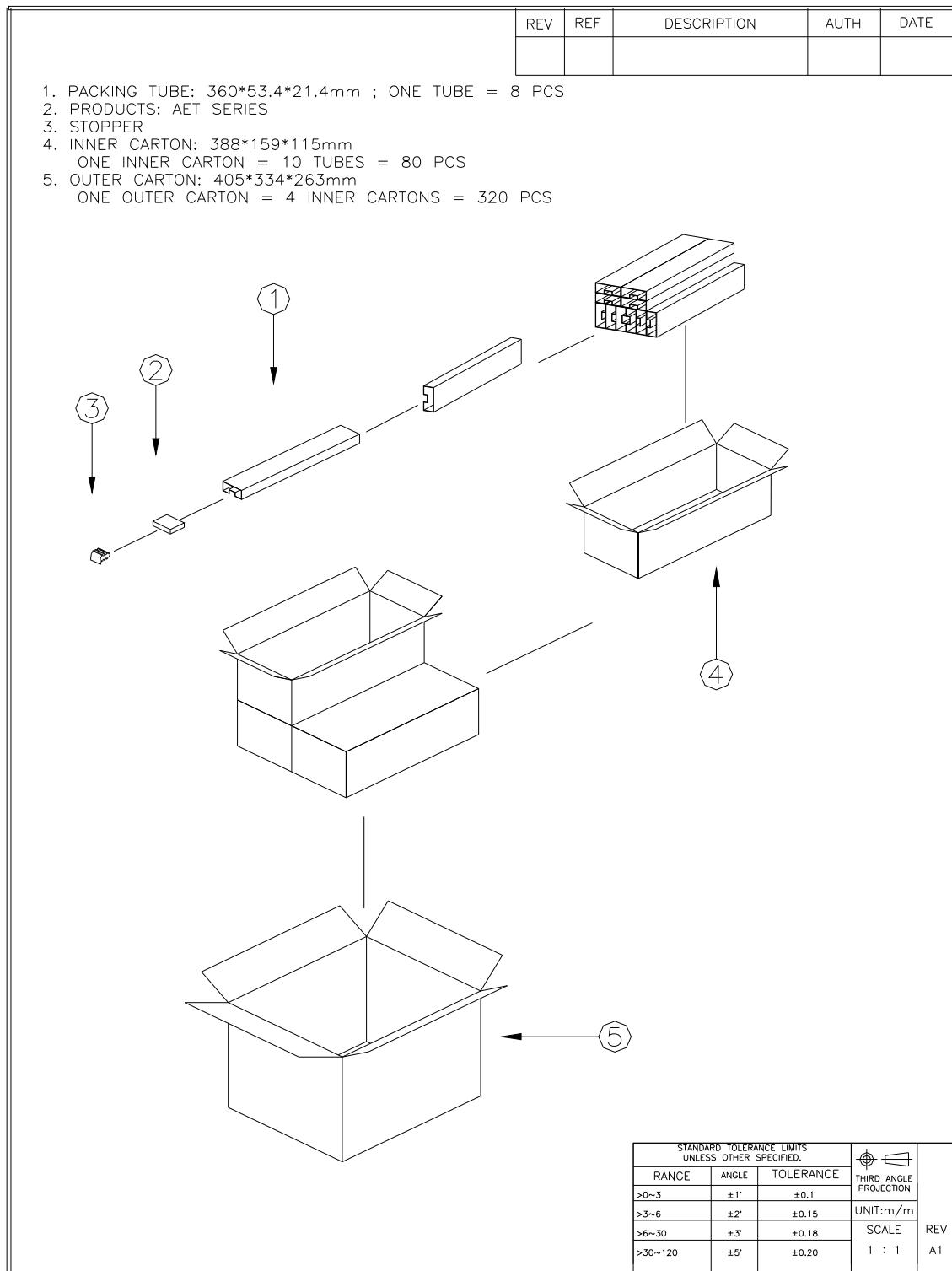


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### Package Information



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**Recommended Lead-Free Wave Soldering Temperature Profile**

